

WHAT IS CLAIMED IS:

1. A liquid crystal display device comprising:
 - a first substrate;
 - a second substrate;
 - a liquid crystal layer interposed between said first substrate and said second substrate;
 - a plurality of video signal lines and scanning signal lines formed on said first substrate, and delimiting pixel regions;
 - a thin film transistor formed in said pixel regions, and driven by a scanning signal from a scanning signal line for supplying video signal from one of the video signal lines to a pixel electrode;
 - a display area containing a plurality of said pixel regions;
 - a first protection element line formed at a peripheral portion of said display area, and being connected to odd-numbered one of said video signal lines by first high-resistance elements;
 - a second protection element line formed at a peripheral portion of said display area, and being connected to even-numbered ones of said video signal lines by second high-resistance elements; and
 - a common line electrically connected to said first and second protection element lines by third high-resistance elements.
2. A liquid crystal display device according to claim 1, wherein at least one of said first, second and third high-resistance elements have a gate electrode connected to a source electrode.

3. A liquid crystal display device according to claim 1, wherein at last one of said first, second and third high-resistance elements is formed by at least one diode in which a gate electrode and a source electrode of a thin-film transistor is connected to each other.

4. A liquid crystal display device according to claim 1, wherein said first, second and third high-resistance elements have a same configuration.

5. A liquid crystal display device comprising:

- a first substrate;
- a second substrate;
- a liquid crystal layer interposed between said first substrate and said second substrate;
- a plurality of video signal lines and scanning signal lines formed on said first substrate, and delimiting pixel regions;
- a thin film transistor formed in said pixel regions, and driven by a scanning signal from the scanning signal line for supplying video signal from one of the video signal lines to a pixel electrode;
- a display area containing a plurality of said pixel regions;
- a first protection element line formed at a peripheral portion of said display area, and being connected to odd-numbered one of said video signal lines by first high-resistance elements; and
- a second protection element line formed at peripheral portion of said display area, and being connected to even-numbered ones of said video signal lines by second high-resistance elements.

6. A liquid crystal display device according to claim 5, wherein at least one of said first and second high-resistance elements having a gate electrode connected to a source electrode.

7. A liquid crystal display device according to claim 5, wherein at least one of said first and second high-resistance elements is formed by at least one diode in which a gate electrode and a source electrode of a thin-film transistor is connected to each other.

8. A liquid crystal display device according to claim 5, wherein said first and second high-resistance elements have a same configuration.